

200504US-



# 11/A  
7/16/02  
Hayes

IN THE UNITED STATES PATENT & TRADEMARK OFFICE

IN RE APPLICATION OF :  
TAKUJI MATSUMOTO ET AL : EXAMINER: OWENS, D.  
SERIAL NO: 09/729,816 :  
FILED: DECEMBER 6, 2000 : GROUP ART UNIT: 2811  
FOR: SEMICONDUCTOR DEVICE AND  
METHOD OF MANUFACTURING  
THE SAME

RECEIVED  
JUL - 8 2002  
TECHNOLOGY CENTER 2800

AMENDMENT

ASSISTANT COMMISSIONER FOR PATENTS  
WASHINGTON, D.C. 20231

SIR:

In response to the Official Action mailed April 11, 2002, please amend this  
application as follows:

IN THE CLAIMS

Please cancel Claims 9-14 without prejudice.

The following is a clean copy of amended Claims 1, 2 and 7, with marked-up copies  
attached:

1. (Amended) A semiconductor device comprising:  
an SOI substrate including a substrate in which at least its surface is insulative and a  
semiconductor layer provided on said surface of said substrate, said semiconductor layer

A  
Sub  
B